

# Peter Miller

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Objective	To obtain a co-op position in order to apply knowledge to real world applications as well as to gain further knowledge and experience in electrical engineering. Available May 2018 - August 2018	
Work Experience	<b>Teledyne Scientific Co. - Durham, NC</b> July 2016 - August 2016 Software Intern - Worked to develop and improve MATLAB scripts for the automation of bulk image processing as part of a team working on a DARPA project. <b>Teledyne Scientific Co. - Durham, NC</b> March 2017 - August 2017 Technical Intern - Further worked to develop and improve MATLAB scripts for bulk image processing as well as for experiment analysis. Trained and helped supervise another intern on use of these scripts. Ran experiments to test and refine software. Helped to prepare slides for review by government. Helped with set up of Nvidia TX1 Jetson development board including compilation of a custom kernel. <b>Advanced Micro Devices - Austin, TX</b> January 2018 - May 2018 Co-op Engineer - Worked to streamline and improve programs for system level testing and developed tools to help with testing. Prepared for and started system level testing of upcoming GPU product.	
Education	Rochester Institute of Technology Bachelor of Science Degree in Electrical Engineering with Computer Engineering Option (Expected 2019) GPA: 2.78/4.00	
Courses	Freshman Practicum Digital Systems I & II Circuits I & II Computational Problem Solving Advanced Programming	EM Fields & Transmission Lines Electronics I & II Embedded Systems Design Design of Computer Systems Classical Controls
Skills	<b>General:</b> Debugging, problem solving, design & verification <b>Languages:</b> Java, C/C++, Assembly, HTML, CSS, JS, MATLAB, VHDL, Verilog, SPICE <b>Hardware:</b> Altera FPGA, TI MSP430 & MSP432, Raspberry Pi, Nvidia TX1	
Projects	<b>0range.xyz</b> -My personal website, developed as a way for me to learn HTML & CSS -Designed to express myself personally and professionally <b>OLLAR &amp; PAM_RISC521 Processors</b> -RISC Processors developed in Verilog for the Altera Cyclone IV FPGA -PAM_RISC521 was developed for Design of Computer Systems course -OLLAR (Open-source License Logic & Arithmetic RISC) is being developed as an open-source RISC processor with goals of being easy to modify and understand -Both processors can implement multiple cores and a four stage pipeline. The PAM_RISC521 implements a fully associative cache. <b>Musical Generator</b> -Written in C++ as a final project for Advanced Programming -Generates music that sounds reasonable -Saves generated music as a MIDI file for manipulation & playback -Wrote my own classes to work more easily with complicated MIDI format <b>Atari 2600 HDMI Modification</b> -Studied schematics for Atari 2600 to figure out where signals were -Took signals and sent them to an ADC designed to output HDMI signal -Future plans to design a filter to clean audio interference from video signal	
Activities	Cum Laude Society Boy Scouts Order of the Arrow Eagle Scout RIT Linux Users Group National Society of Leadership & Success	2014-Present 2006-2014 2009-Present 2014 2014-Present 2016-Present

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